

FIG. 1

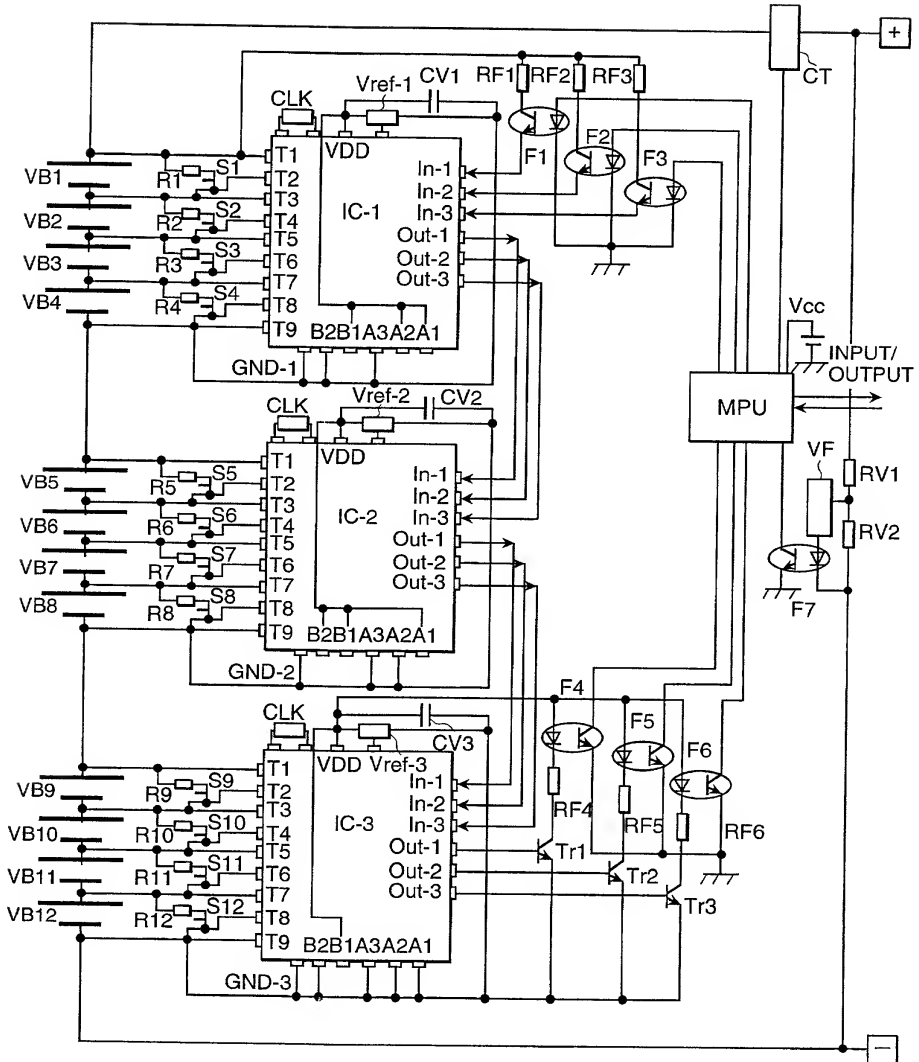


FIG. 2

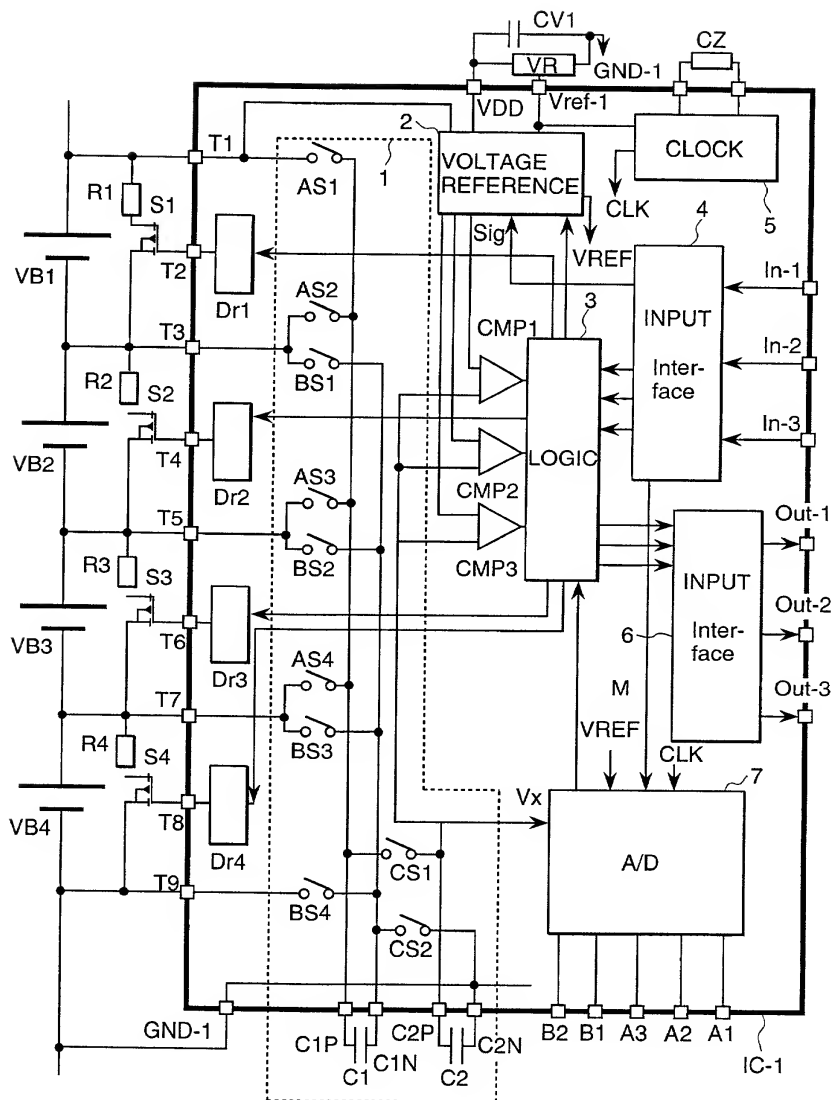


FIG. 3

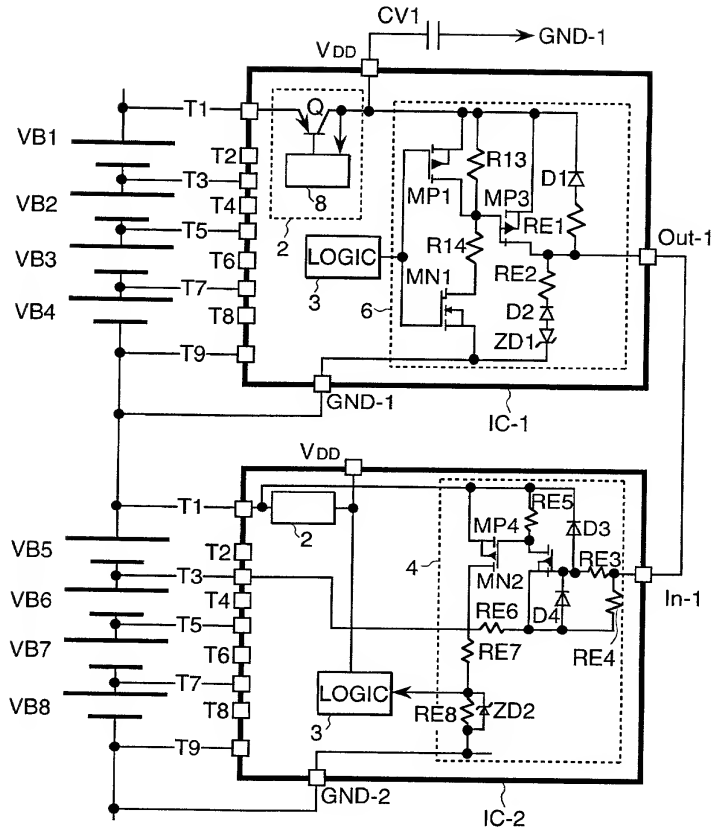


FIG. 4

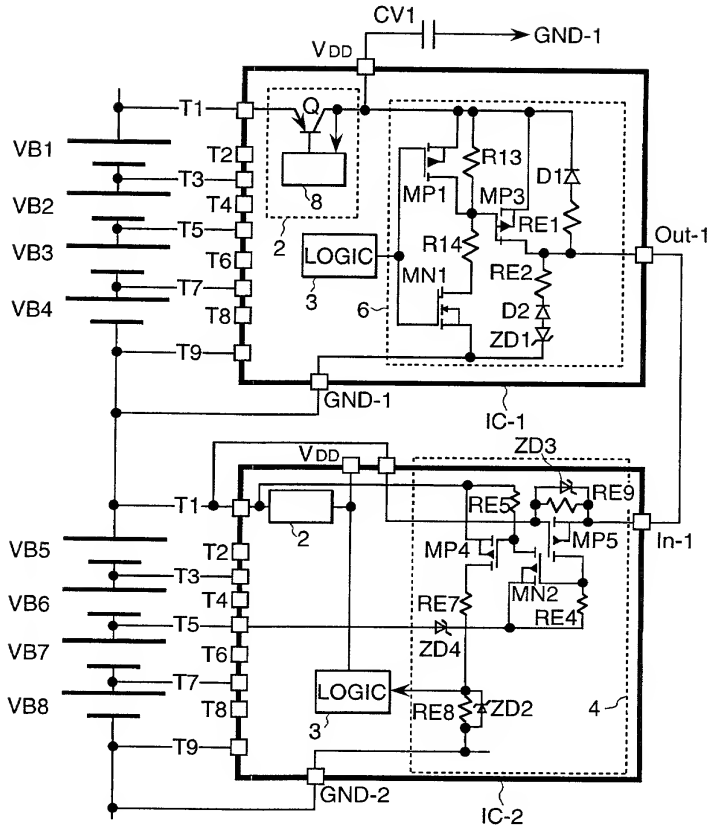


FIG. 5

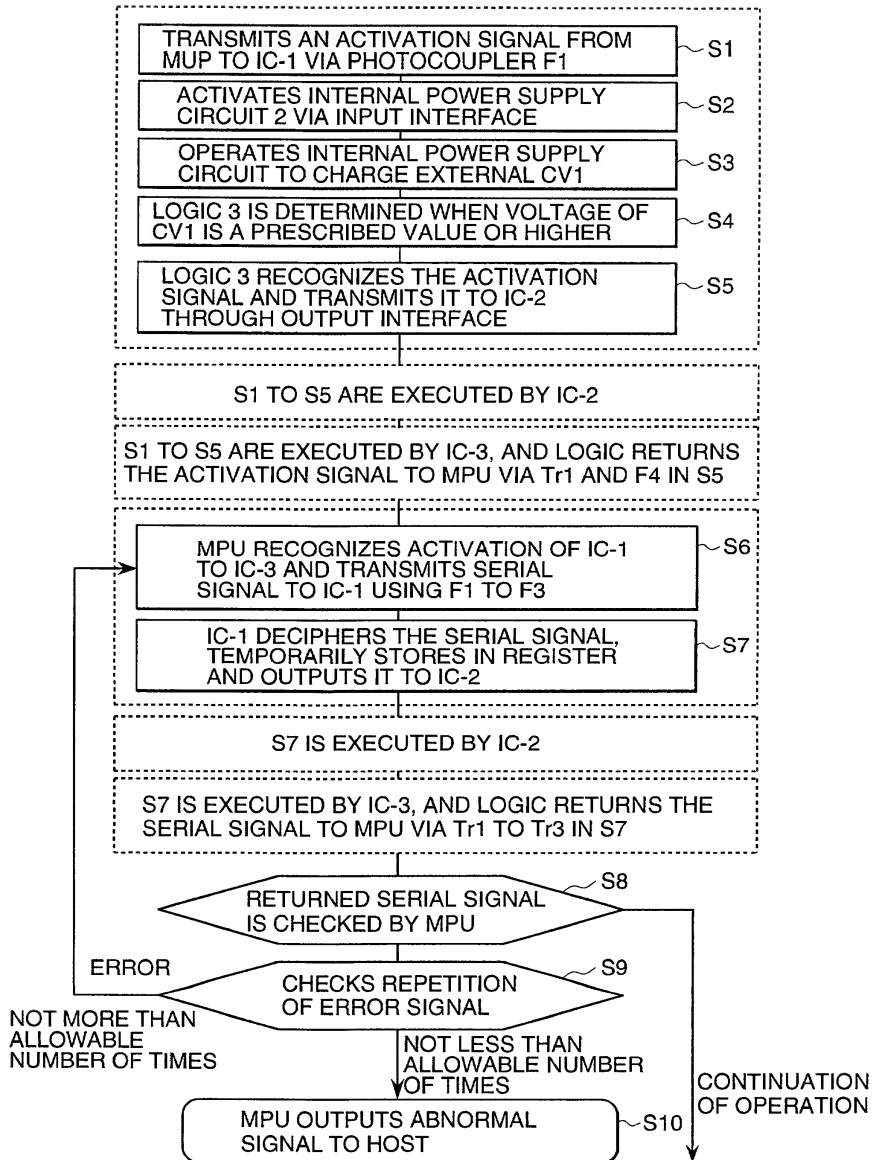
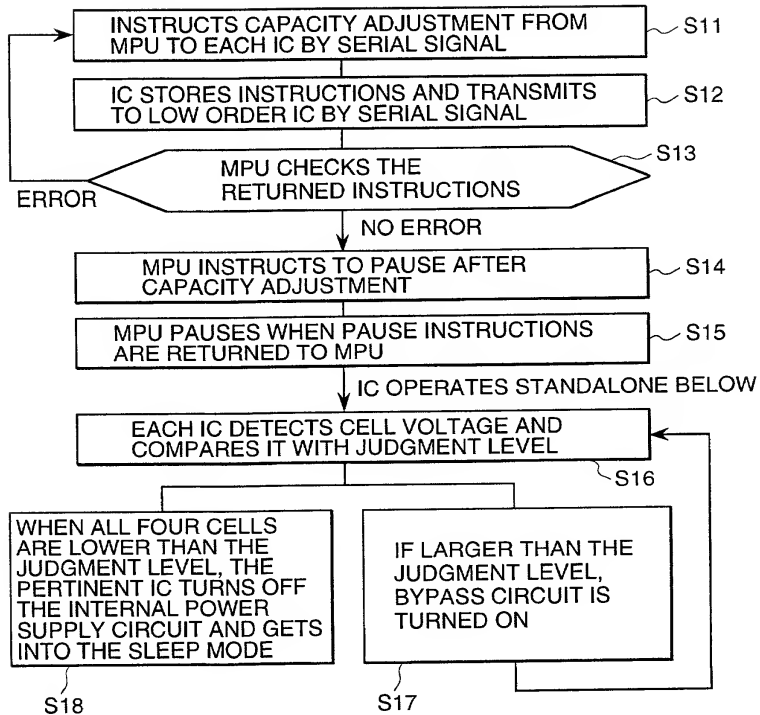


FIG. 6



[illegible]

OUT P1 CNT1

OFFSET COMPENSATION

FIG. 9

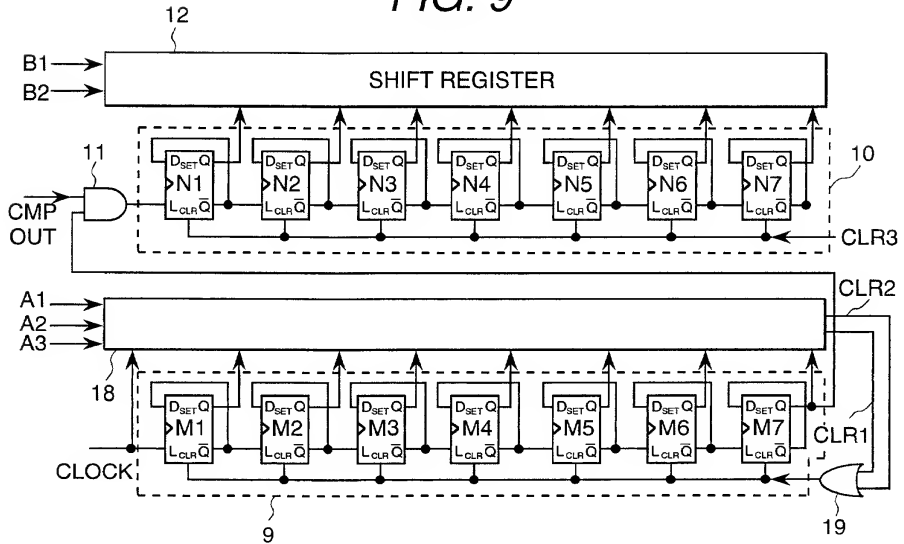


FIG. 10

No.	A1	A2	A3	CLK	M1 Q	M2 Q	M3 Q	M4 Q	M5 Q	M6 Q	M7 Q	CLR 1	CLR 2
1	—	0	0	0	—	—	—	—	—	—	1	1	0
2	1	1	0	0	1	—	—	—	—	—	1	0	1
3	1	0	1	0	0	1	—	—	—	—	1	0	1
4	1	1	1	0	1	1	—	—	—	—	1	0	1
5	0	1	0	0	1	1	1	1	1	1	0	0	1
6	0	0	1	0	0	1	1	1	1	1	0	0	1
7	0	1	1	0	1	0	1	1	1	1	0	0	1



FIG. 11

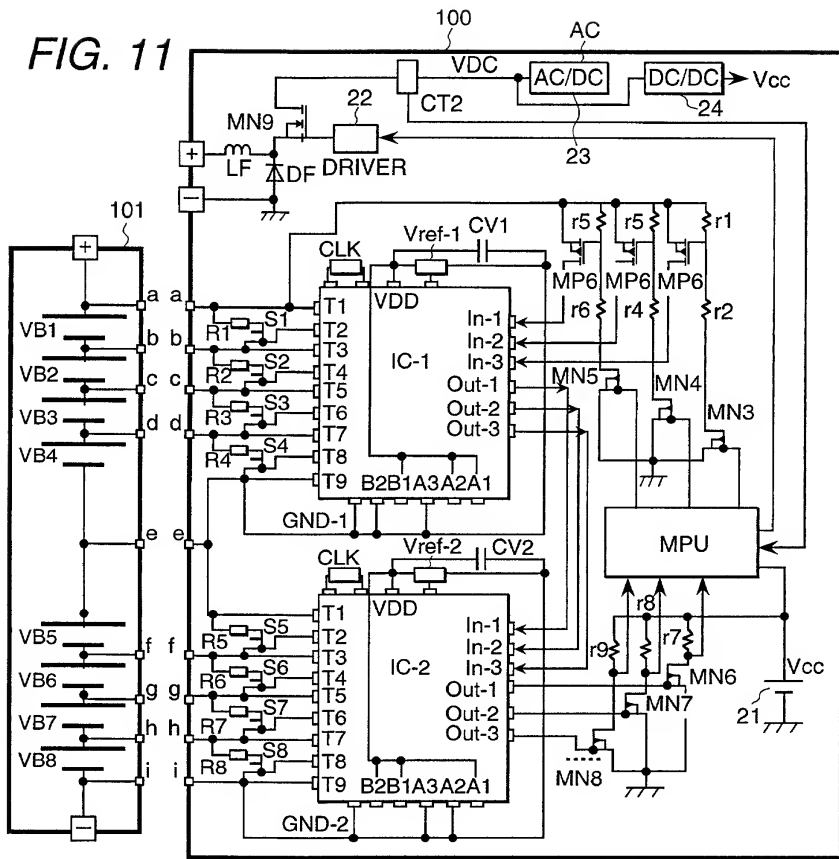


FIG. 12A



FIG. 12B

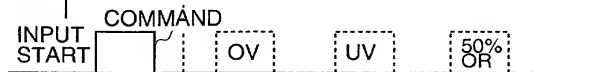


FIG. 12C



FIG. 12D

